

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/607,869
Filing Date June 27, 2003
Inventor Zhongze Wang
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Jennifer M. Kennedy
Attorney Docket No. MI22-2343
Title: Method of Forming Silicon-on-Insulator Comprising Integrated Circuitry

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

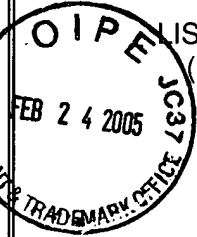
Respectfully submitted,

Dated:

February 24, 2005

By:

Jennifer J. Taylor
Jennifer J. Taylor, Ph.D.
Reg. No. 48,711

Form PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE					ATTY. DOCKET NO. MI22-2343		SERIAL NO. 10/607,869	
 <p>LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)</p>					APPLICANT: Zhongze Wang			
					FILING DATE 06/27/2003		GROUP 2812	
U.S. PATENT DOCUMENTS								
*Examiner's Initials		Document Number	Date	Name	Class	Sub- class	Filing Date If Appropriate	
	AA	6,215,155 B1	04-2001	Wollesen				
	AB	6,268,630 B1	07-2001	Schwank et al.				
	AC	6,340,829 B1	01-2002	Hirano et al.				
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	AE	6,642,579 B2	11-2003	Fung				
	AF							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Sub- class	Translation	
							Yes	No
	AJ							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
	AM							
EXAMINER		DATE CONSIDERED						
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>								